

Figure 1a

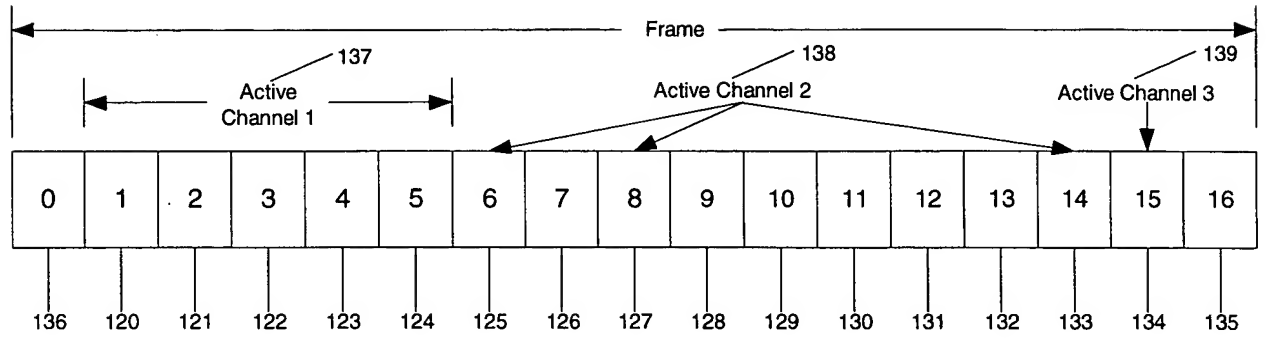


Figure 1b

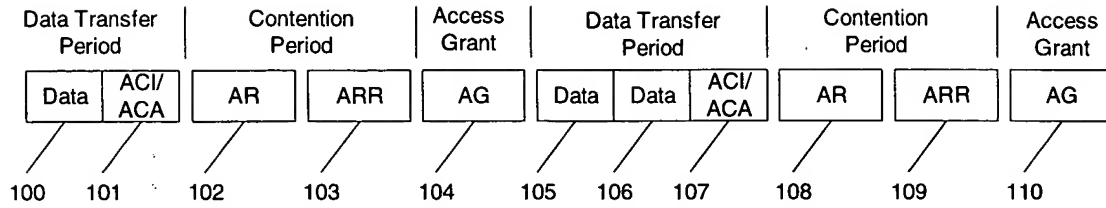


Figure 1c

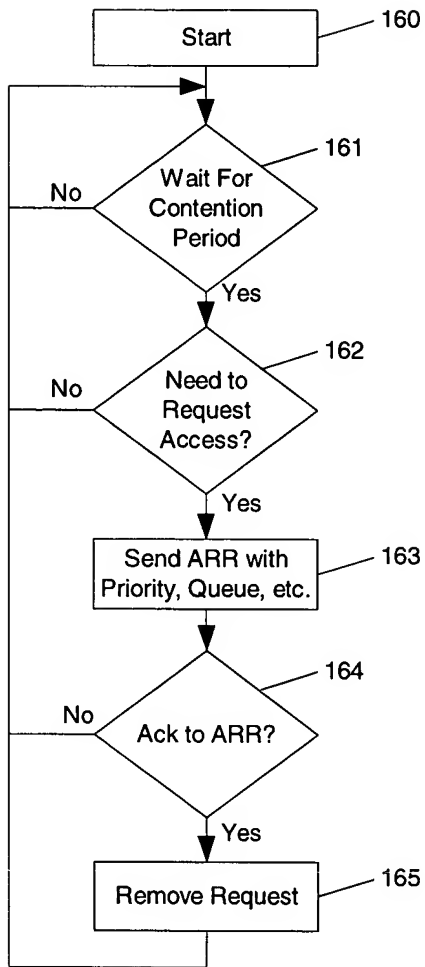


Figure 1d

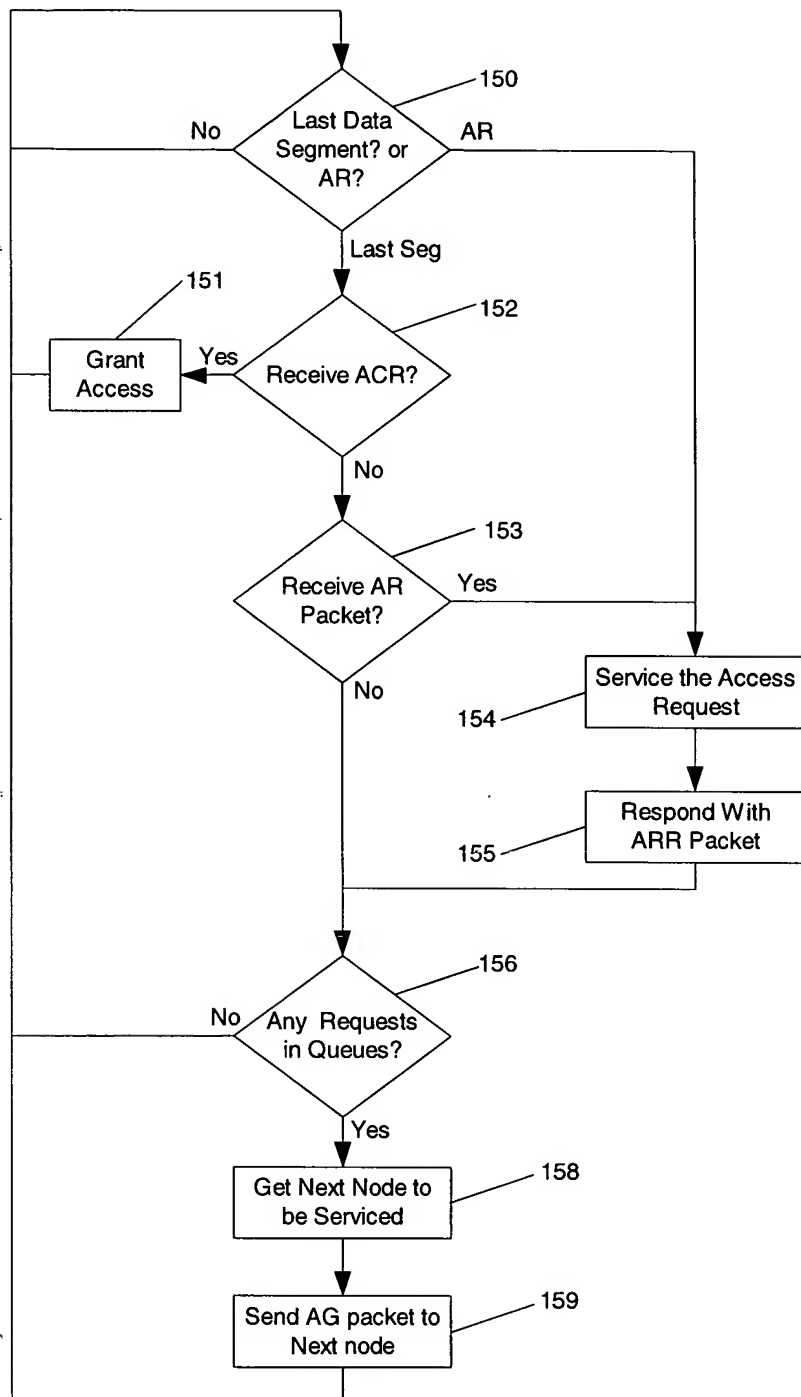


Figure 1e

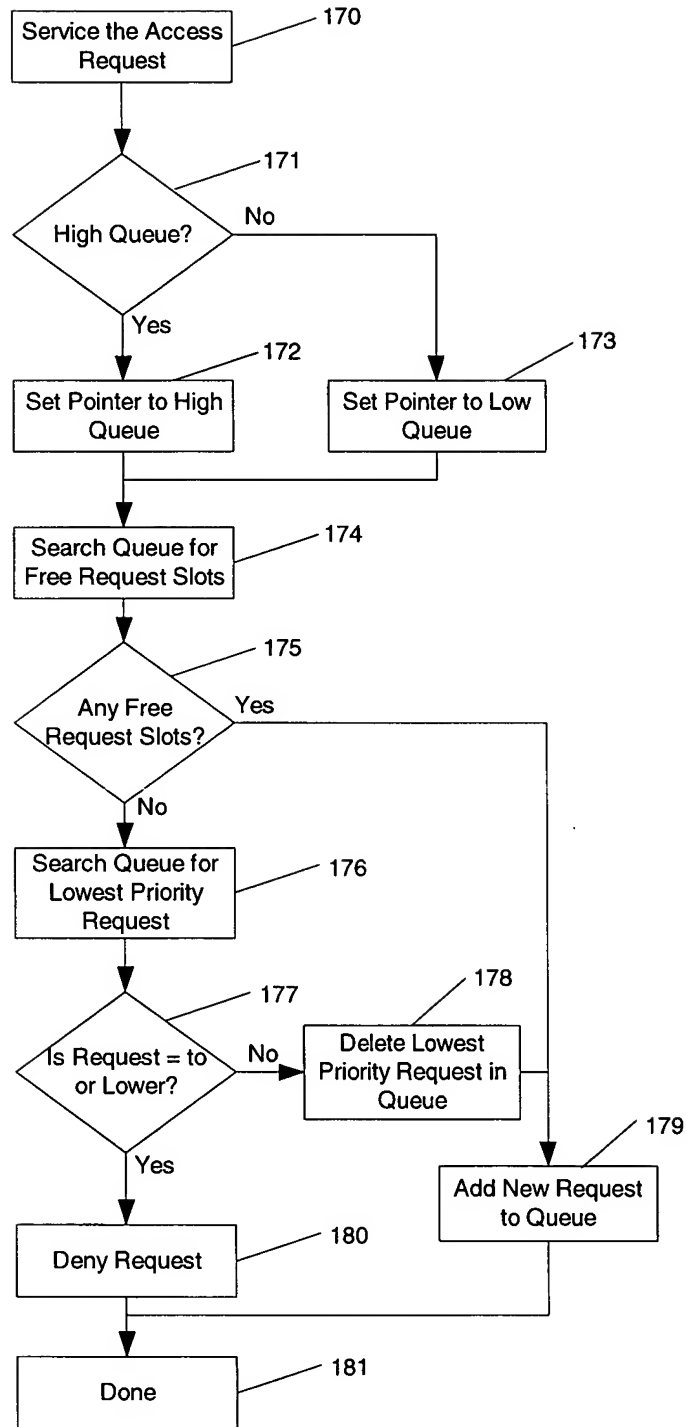


Figure 1f

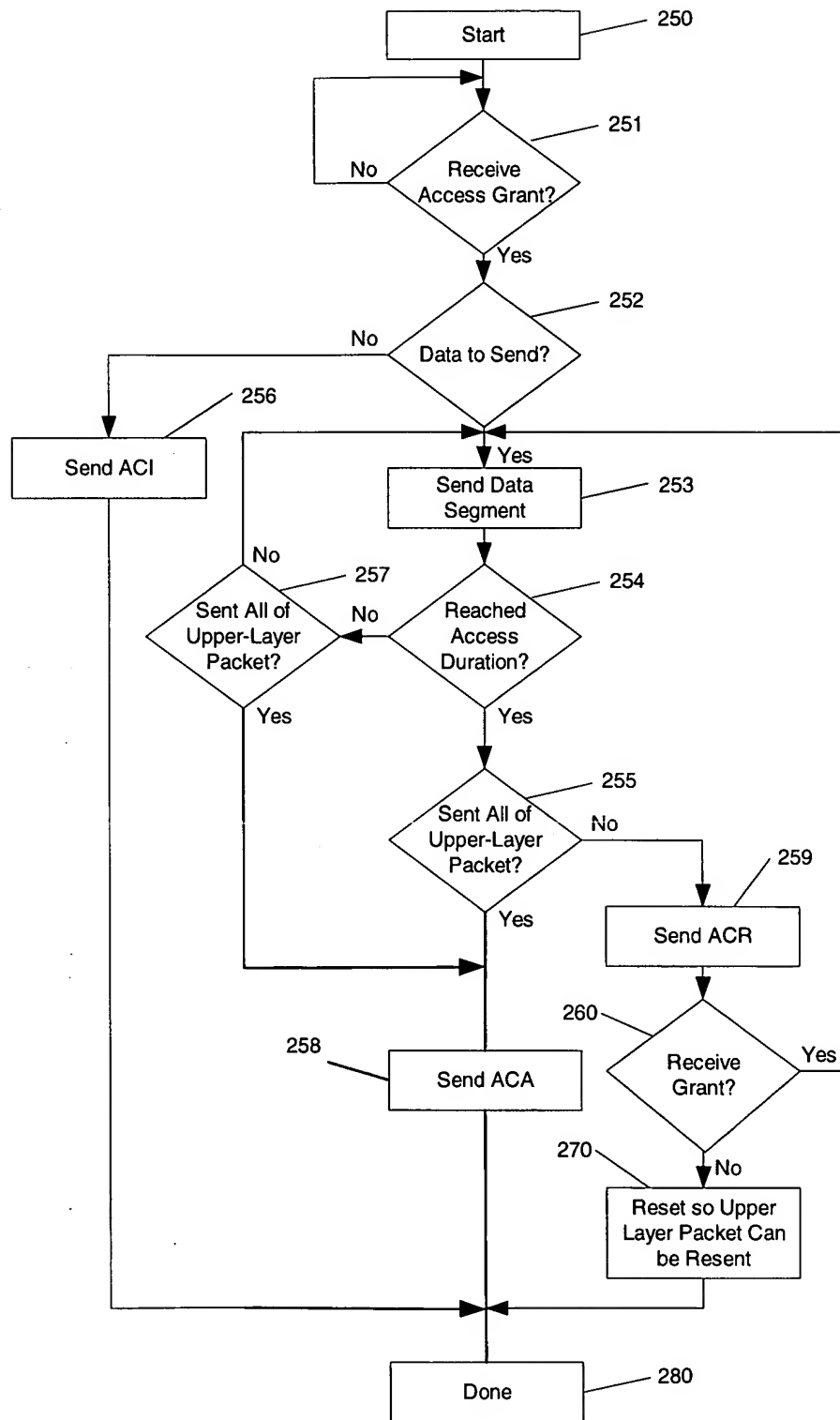


Figure 2a

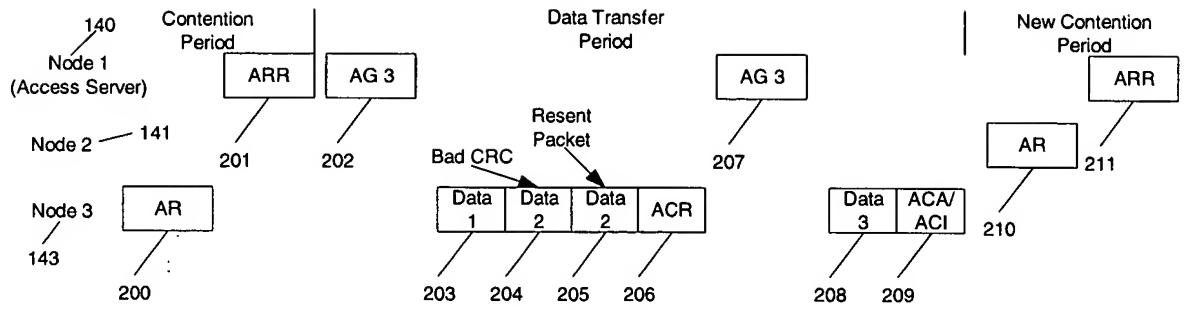


Figure 2b



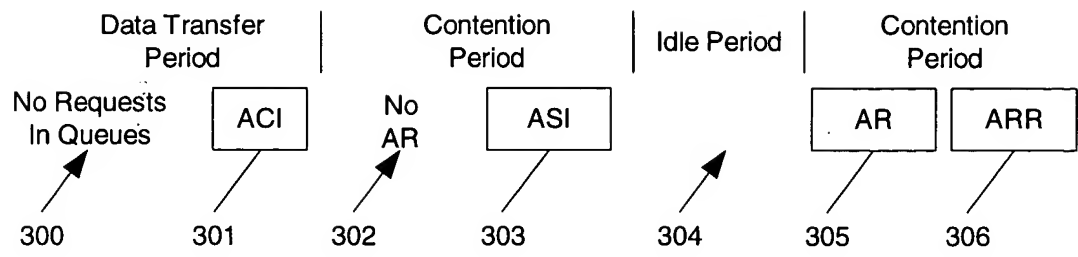


Figure 3

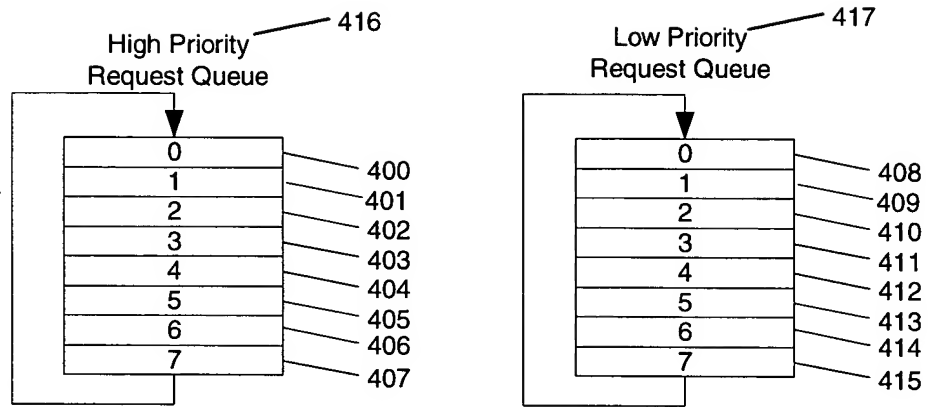


Figure 4a

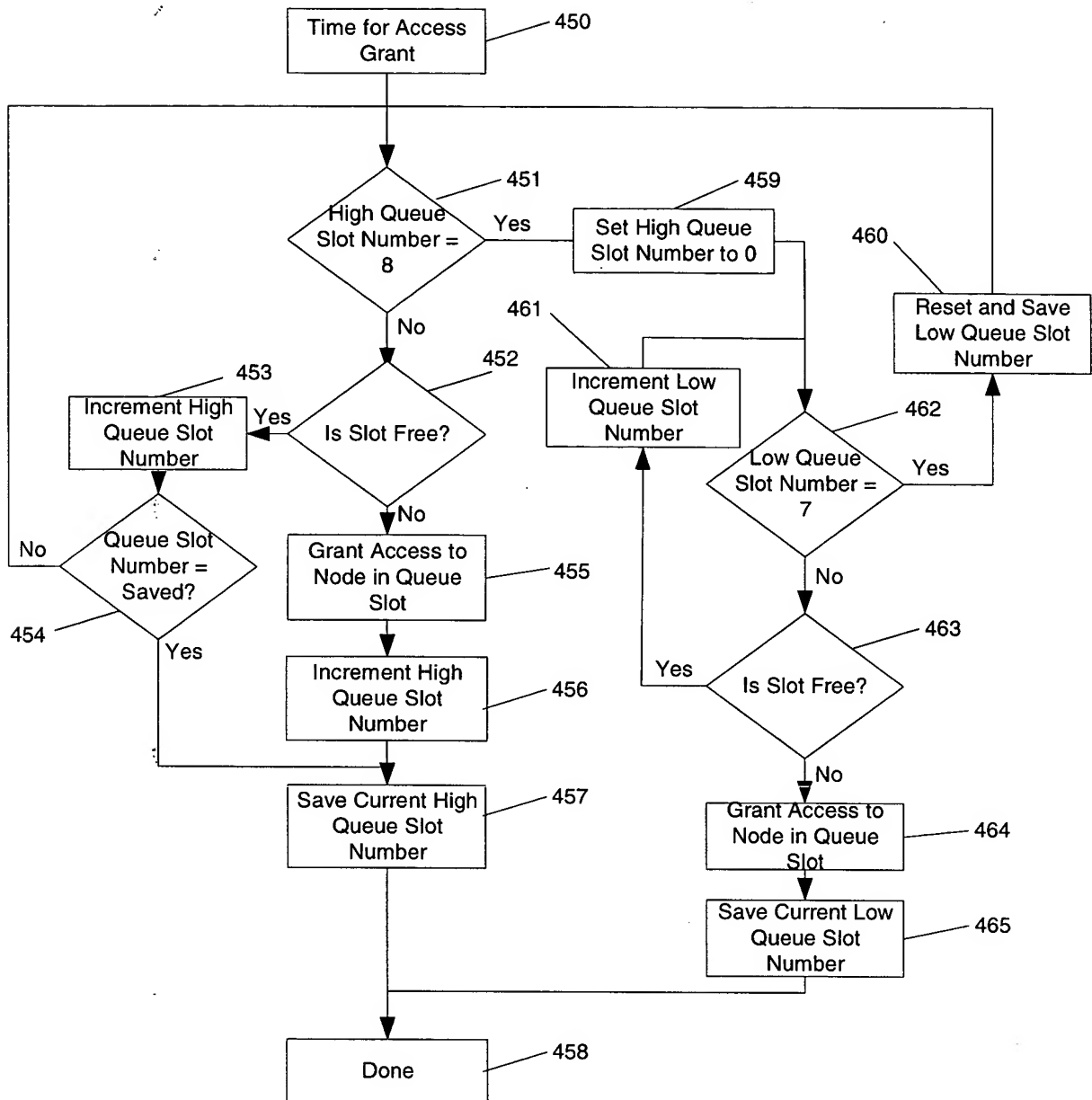


Figure 4b

	501	503	505	507	509	511	513	515									
	500	502	504	506	508	510	512	514	516								
Packet	A	A	I	I	A	I	NA	NA	NA	NA	A	I	I	I	I	NA	I
TTL Value	5	5	4	3	5	4	3	2	1	0	5	4	3	2	1	0	F
	517	518	519	520	521	522	523	524	525	526	527	528	529	530	531	532	533

Figure 5

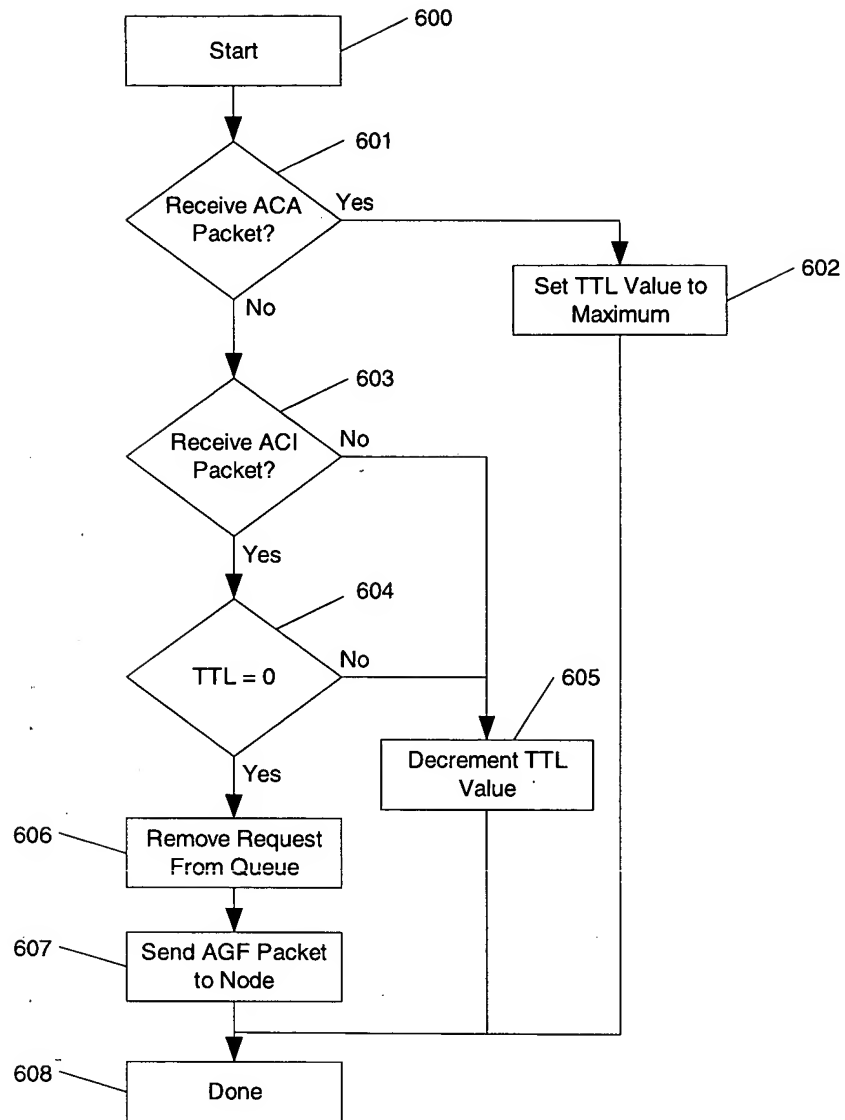


Figure 6

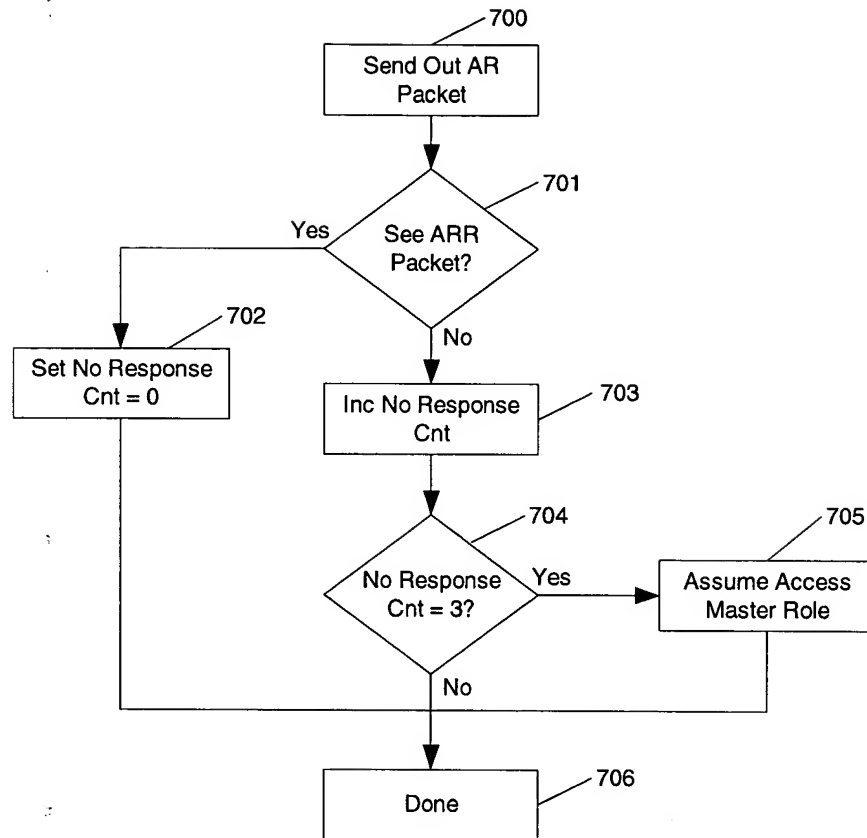


Figure 7

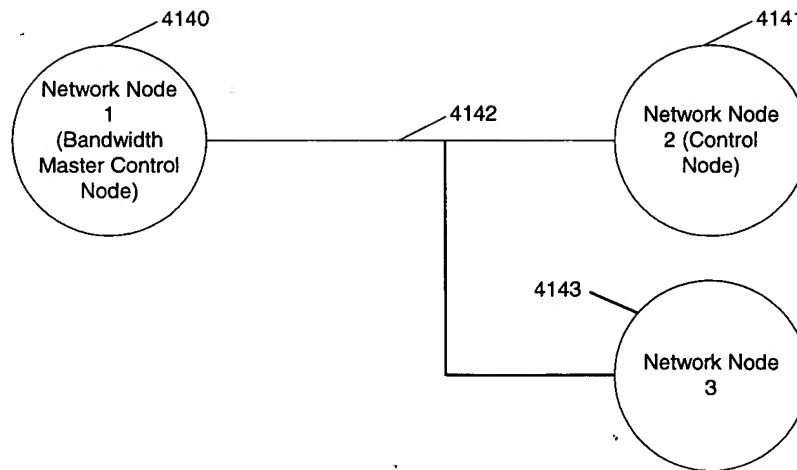


Figure 8

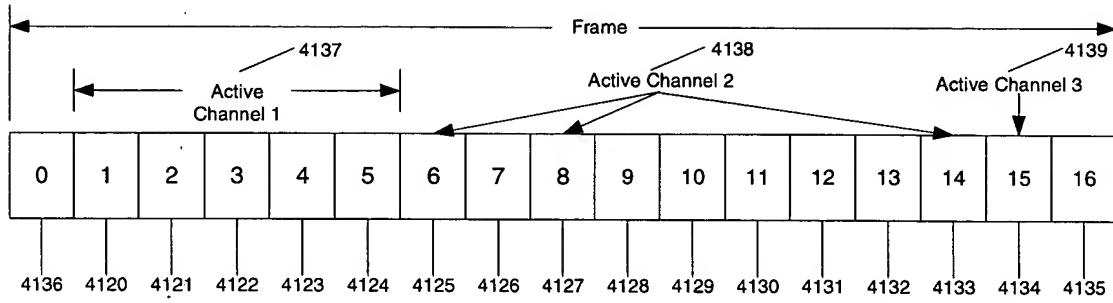


Figure 9



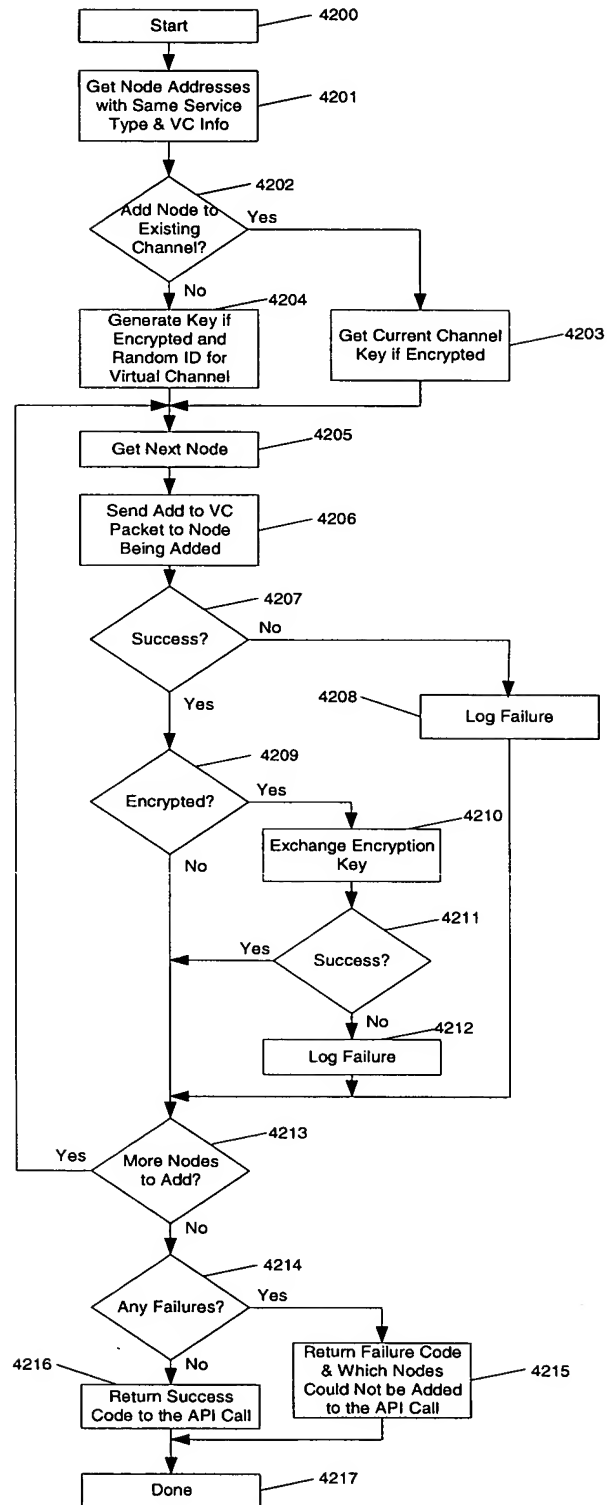


Figure 10

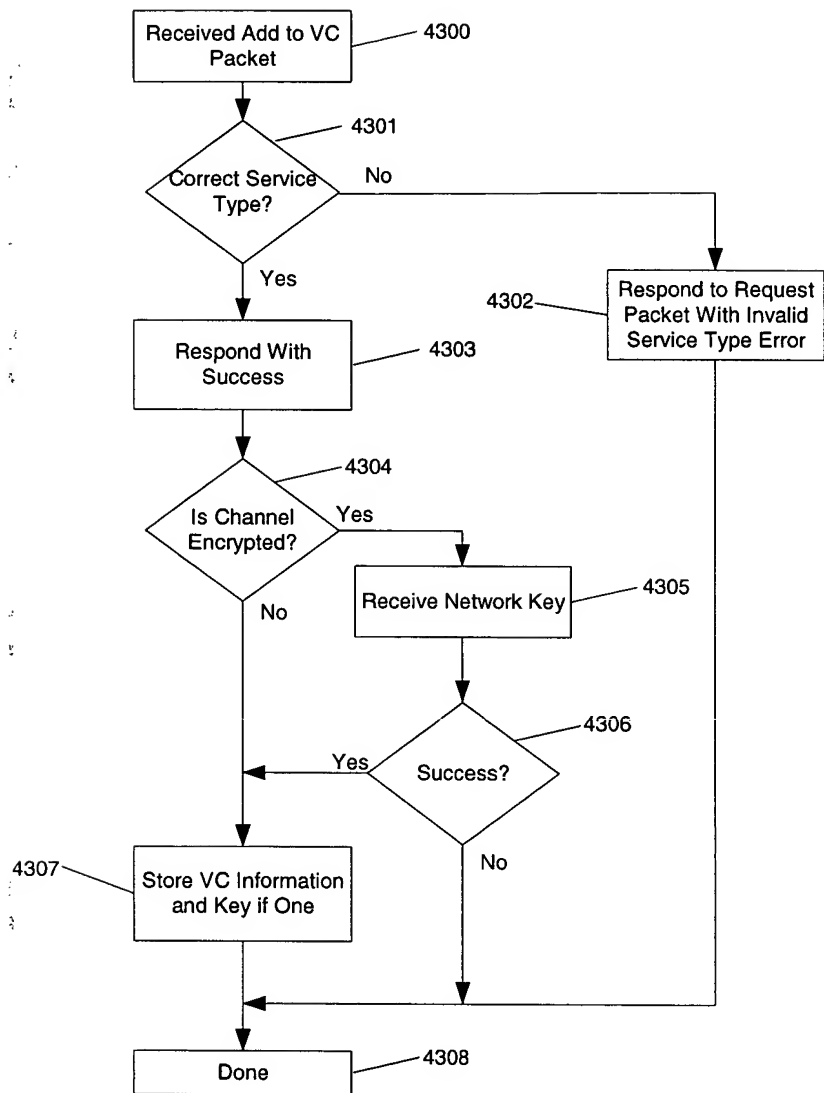


Figure 11

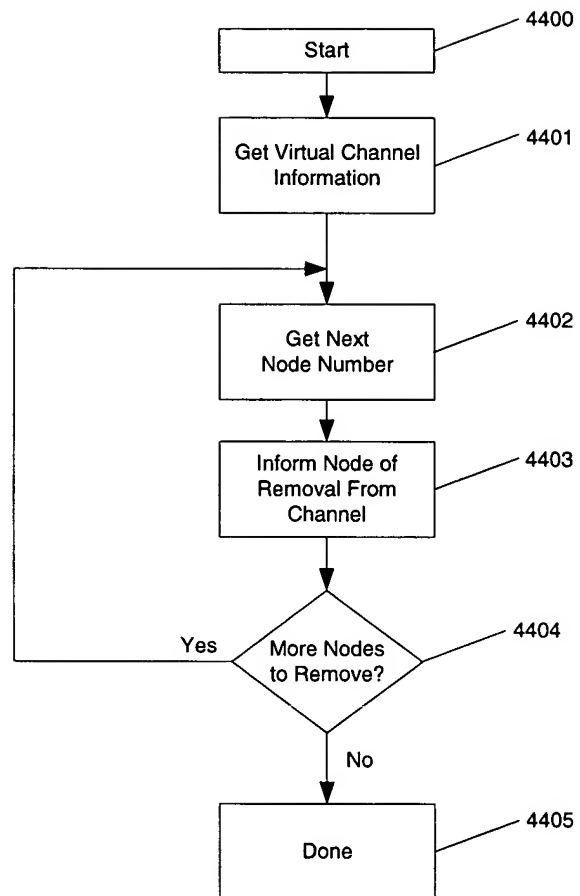


Figure 12

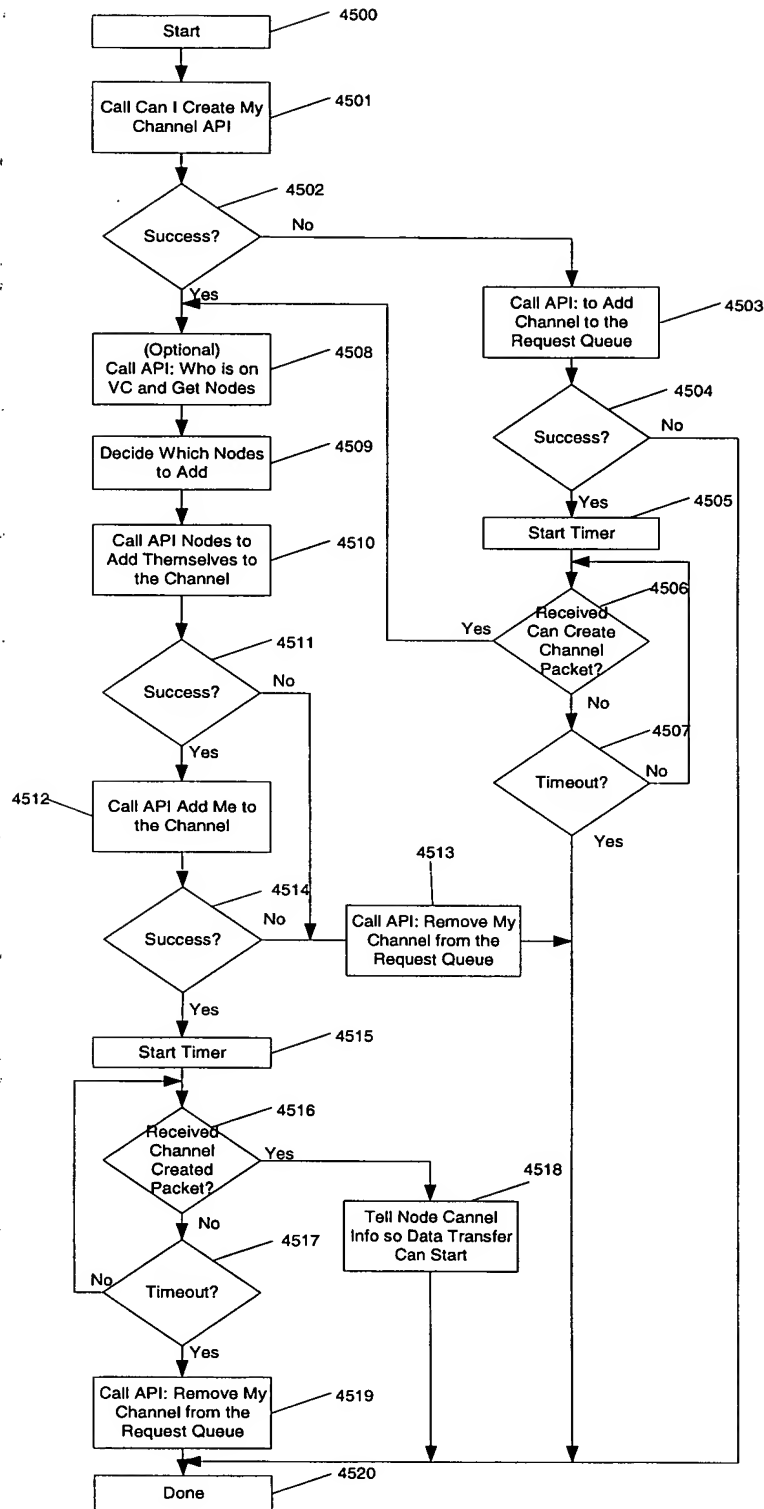


Figure 13

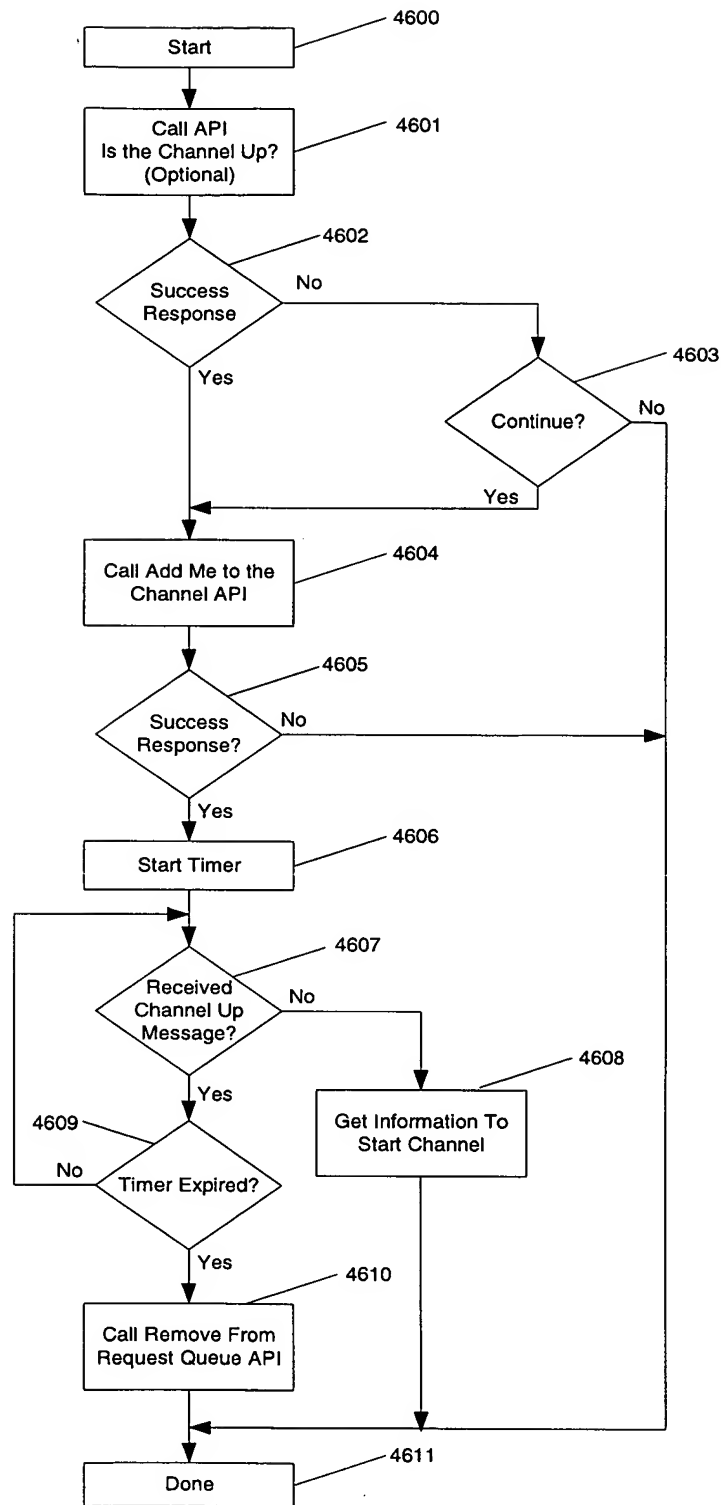


Figure 14

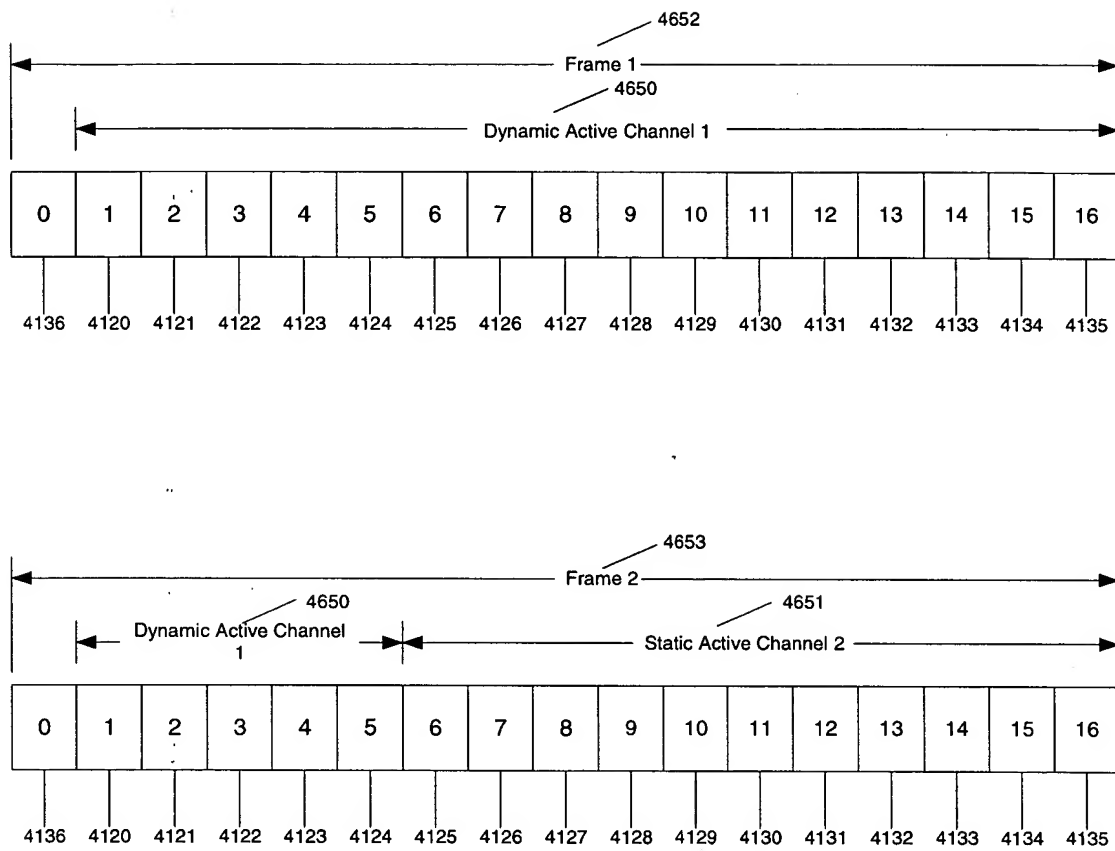


Figure 15

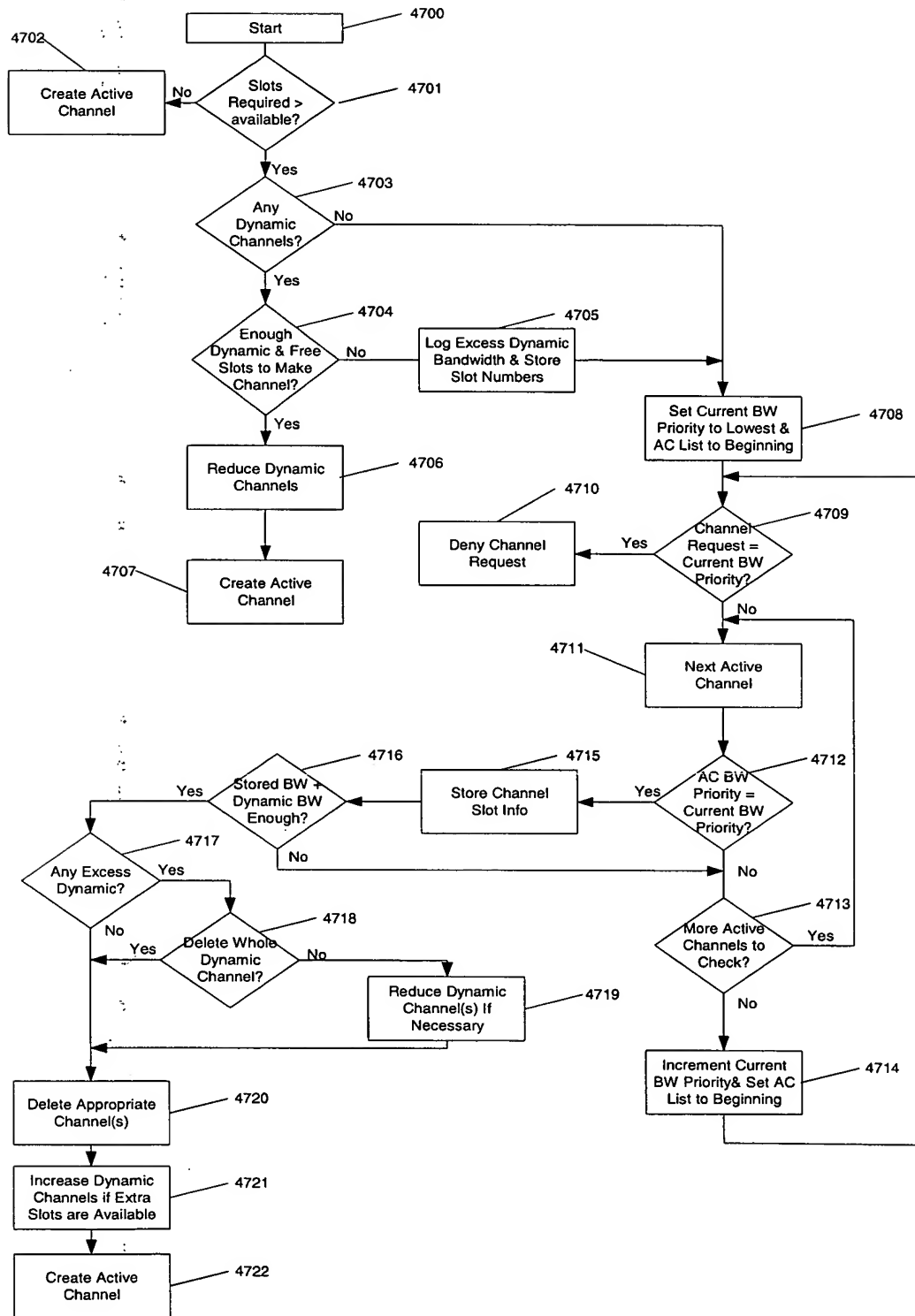


Figure 16

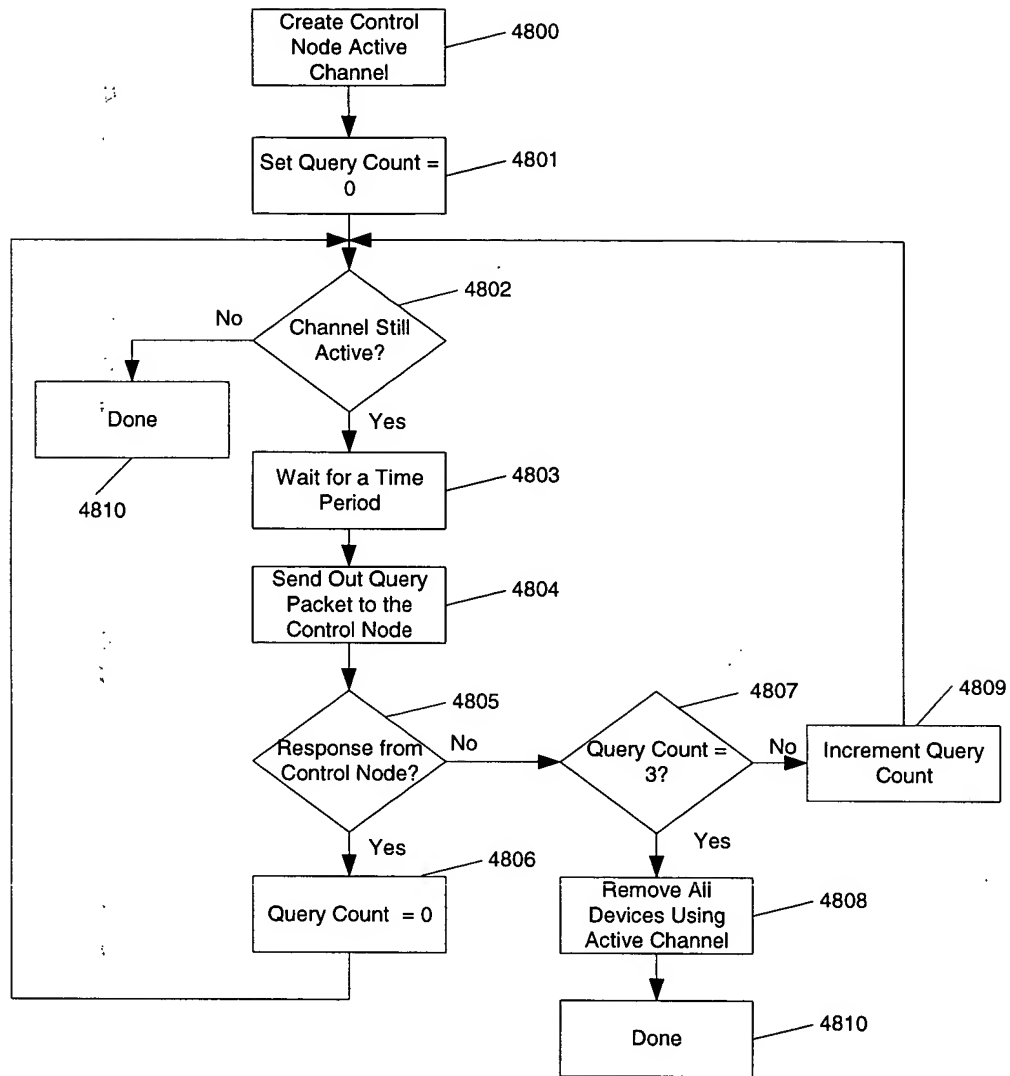


Figure 17



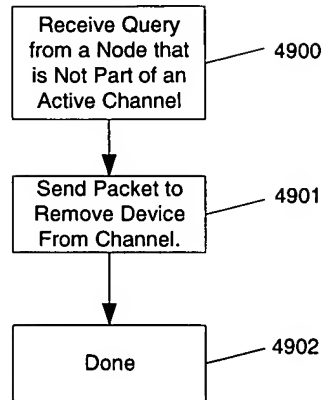


Figure 18

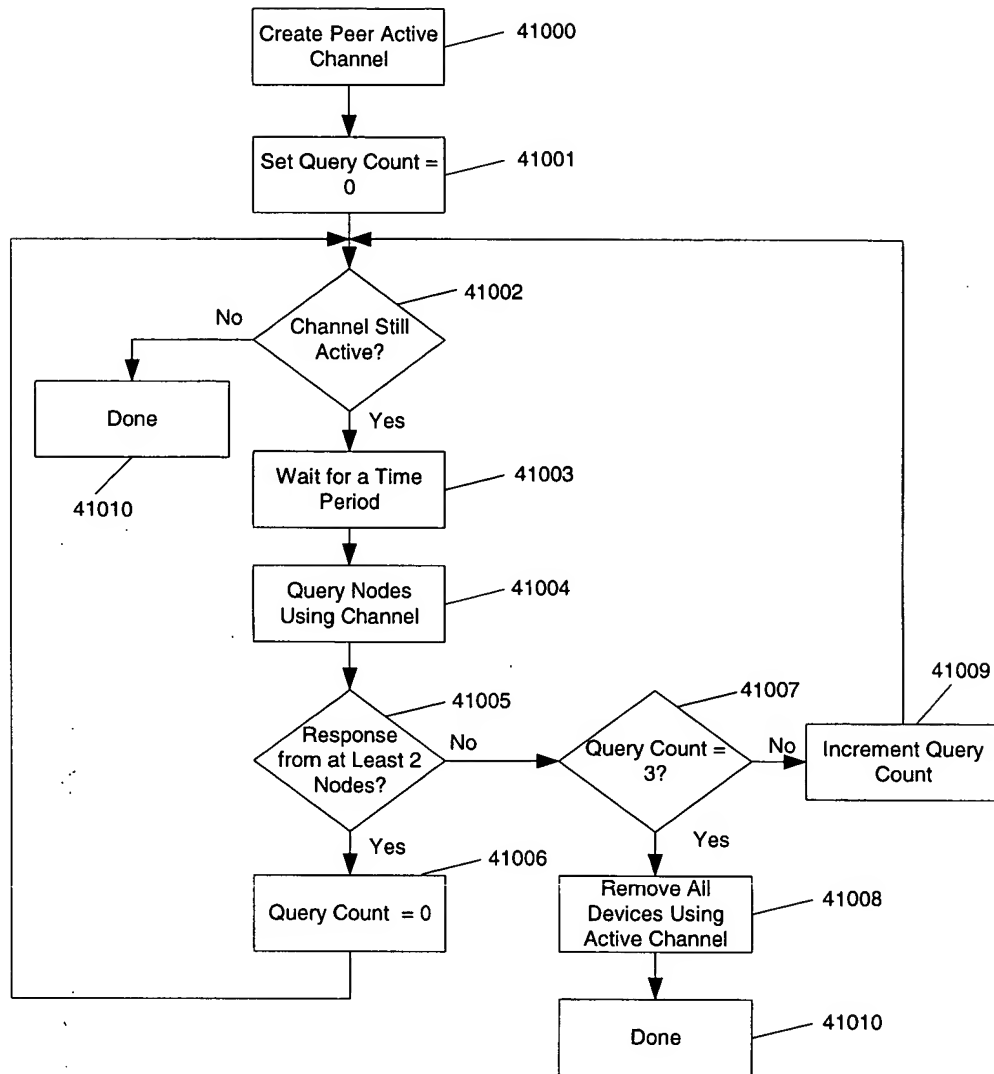


Figure 19